

App. Serial No.: 10/801,942
Atty. Docket No.: 0057-011

IN THE CLAIMS

Please amend the claims as follows:

1. (currently amended) A computer array, comprising:
a plurality of computers; and
and a plurality of data paths connecting the computers, the data paths being hard wired
between associated pairs of the computers; wherein: and wherein,
at least some of the computers are assigned a task different from that assigned to the other
computers.
2. (original) The computer array of claim 1, wherein:
each of the computers is assigned a task different from that of the other computers.
3. (original) The computer array of claim 1, wherein:
at least some of the computers are configured for specific input functions.
4. (original) The computer array of claim 1, wherein:
at least some of the computers are configured for specific output functions.
5. (original) The computer array of claim 1, wherein:
communication between the computers is asynchronous.
6. (original) The computer array of claim 1, wherein:
communication between the computers is via a plurality of parallel data lines.
7. (original) The computer array of claim 1, wherein:
each of the computers is hard wired to communicate with at least three of the plurality of
computers.
8. (original) The computer array of claim 1, wherein:
the quantity of computers is 25.

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9. (original) The computer array of claim 1, wherein:
the computers are physically arrayed in a 5 by 5 array.
10. (original) The computer array of claim 1, wherein:
at least some of the computers are physically arrayed in a 4 by 6 array.
11. (original) The computer array of claim 1, wherein:
the quantity of computers along each side of the array is an even number.
12. (original) The computer array of claim 1, wherein:
at least one of the computers is in direct communication with an external memory source.
13. (original) The computer array of claim 1, wherein:
at least one of the computers communicates data from an external memory source to at least some of the plurality of computers.
14. (currently amended) A method for performing a computerized job, comprising:
providing a plurality of computers interconnected via discrete sets of data lines, each set of data lines being dedicated to a particular pair of the computers; and
assigning a different task to at least some of the computers; and
executing the tasks on the assigned computers.
15. (original) The method of claim 14, wherein:
at least one of the computers is assigned to communicate with a flash memory.
16. (original) The method of claim 14, wherein:
at least one of the computers is assigned to communicate with a random access memory.
17. (original) The method of claim 14, wherein:
at least one of the computers is assigned to accomplish an input/output function.

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18. (original) The method of claim 14, wherein:
one of the computers routes assignments to the remainder of the computers.
- 19.(currently amended) A computer array, comprising:
a plurality of computers; and
a plurality of data connections between the computers, each of the data connections being directly accessible to no more than two of the computers; wherein
at least some of the computers are programmed to perform different functions.
20. (original) The computer array of claim 19, wherein:
the different functions work together to accomplish a task.
21. (original) The computer array of claim 19, wherein:
each of the functions is programmed into the respective computers when the computer array is initialized.
22. (original) The computer array of claim 19, wherein:
communication between the computers is asynchronous.
23. (currently amended) A method for accomplishing a task using a plurality of computers, comprising:
providing the plurality of computers interconnected by data lines, each of the data lines being accessible to no more than two of the computers;
dividing a task into operational components and assigning each of the operational components to one of the computers;
programming at least some of the computers to accomplish each of the operational components; and
executing the operational components on the assigned computers.

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- 24.(currently amended) The method for accomplishing a task of claim 23, wherein:
the operational components are operations used in accomplishing functions of a global positioning system receiver.
25. (original) The method for accomplishing a task of claim 23, wherein:
before the task is begun, programming the computers to accomplish each of the operational components.
26. (original) The method for accomplishing a task of claim 23, wherein:
the computers are arranged in a computer array.
27. (new) The computer array of claim 1, wherein:
at least some of the data paths are connected to no more than two of the computers.
28. (new) The computer array of claim 1, wherein:
each of the data paths is dedicated to an adjacent pair of the computers.
29. (new) The computer array of claim 1, wherein:
the computers operate asynchronously.
30. (new) The computer array of claim 1, wherein:
each of the computers includes dedicated memory.
31. (new) The computer array of claim 30, wherein:
the dedicated memory includes random access memory.
32. (new) The computer array of claim 30, wherein:
the dedicated memory includes read only memory.
33. (new) The computer array of claim 32, wherein:
the dedicated memory includes random access memory.

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34. (new) The computer array of claim 1, wherein:
each of the computers is an independently functioning computer.
35. (new) The computer array of claim 1, wherein:
the computers operate asynchronously; and
the computers communicate with each other asynchronously.
36. (new) The computer array of claim 35, wherein:
each of the computers includes dedicated memory.
37. (new) The computer array of claim 36, wherein:
the dedicated memory includes random access memory.
38. (new) The computer array of claim 37, wherein:
the dedicated memory includes read only memory.
39. (new) The computer array of claim 1, wherein:
the computers are integrated in a single unitary substrate.
40. (new) The computer array of claim 1, wherein:
the computers are substantially the same.
41. (new) The computer array of claim 40, wherein:
at least some of the computers are mirror images of adjacent computers.

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42. (new) A computer array, comprising:
- a plurality of computers; and
 - and a plurality of data paths connecting the computers; and wherein
 - at least some of the computers are assigned a task different from that assigned to the other computers and
 - at least some of the computers include dedicated memory for the exclusive use of an associated one of the computers.
43. (new) A computer array, comprising:
- a plurality of computers operating asynchronously; and
 - and a plurality of data paths connecting the computers; and wherein
 - at least some of the computers are assigned a task different from that assigned to the other computers.
44. (new) The computer array of claim 43, wherein:
- the computers communicate with each other asynchronously.